



Michael J. Fister

**President and Chief Executive Officer
Cadence Design Systems, Inc.**

Mike Fister is President and Chief Executive Officer of Cadence Design Systems, Inc. Prior to joining Cadence, he spent 17 years at Intel Corporation, where he was most recently Senior Vice President and General Manager of the company's Enterprise Platforms Group.

Intel's Enterprise Platform Group designs, markets, and supports building blocks for enterprise computing. During Fister's tenure as General Manager, Intel garnered commanding market segment share, including the introduction of the

Itanium processor family which leads in virtually every performance category. Other products included a complement of Xeon processors, chipsets, boards/systems, and software tools and services.

Previously, Fister served as Vice President and General Manager of the Performance Microprocessor Group, where he managed Intel's IA-32 processor development organization and was responsible for the design, development and marketing of IA-32 processors, including the last versions of the Intel486 and the entire line including the Pentium Pro, Pentium II, Pentium III, Celeron, Pentium II Xeon, and Pentium III Xeon processors. Prior to this role, Mike held many other product development positions at Intel and has a long legacy of successful technology development and product delivery.

Fister is a graduate of the University of Cincinnati where he received a BS and MS in electrical engineering in 1977 and 1978 respectively. He spent his early years in a variety of executive and engineering management positions at Wyse, Machine Vision International, and Cincinnati Milacron. He currently sits on the Board of Directors of Autodesk Corporation.