



### **Aurangzeb Khan**

**Corporate Vice President, Business Development**

Aurangzeb Khan is corporate vice president for business development at Cadence. Earlier, he was CVP/GM for Cadence Design Foundry (now part of engineering services), and focused on the IC design business, including IP development and integration. Khan joined Cadence Design Systems in 2002 when Cadence acquired Simplex Solutions, where he served as executive vice president and general manager of Simplex's SoC Design Foundry. Prior to that, Khan was President and CEO of Altius Solutions. He has over 18 years of management experience and over 25 years of product development experience.

Before founding Altius, Khan was the vice president of silicon engineering at Cirrus Logic and the director of VLSI technology at Tandem Computers. He also developed high-speed SRAMs at Fairchild Semiconductor. Khan helped deliver several generations of high-performance systems and ICs, such as the Sony GS<sup>®</sup>I-32 and PlayStation<sup>®</sup>2 Graphics Synthesizers, the Cirrus Logic 3Ci<sup>™</sup> SOC and the NonStop<sup>™</sup> Himalaya<sup>™</sup> servers. Several products achieved ~\$200M to \$2B in annual revenues. He has published extensively on business and technology development and holds eight patents in high-speed circuit design.

Khan holds two M.S. degrees from Stanford University, in Engineering Management and Electrical Engineering, as well as a double-major B.S. in Electrical Engineering and Computer Sciences and Nuclear