



Jim Miller

**Senior Vice President, Development
Cadence Design Systems, Inc.**

Jim Miller serves as Cadence Senior Vice President of Development reporting to Michael J. Fister, President and CEO. Miller heads cross-product Engineering and is responsible for product development at Cadence.

Miller came to Cadence with 20 years of experience in engineering and management. He was most recently with Intel Corporation where he was responsible for both an upcoming MP platform and server memory technology for the Enterprise Products Group. Previously, Miller held a number of other

leadership roles at Intel, including management of the server and workstation chipset organization, the Itanium processor, and Pentium II processor.

Previous positions held by Miller include Vice President of Silicon Development at VoIP pioneer Silicon Spice, and later Technical Director with Broadcom following its acquisition of Silicon Spice.

Miller graduated from Stanford University with both a B.S. and M.S degree in Electrical Engineering and holds an MBA from Santa Clara University.